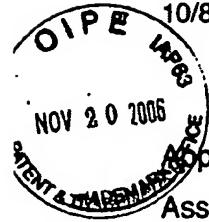


IN THE UNITED STATES PATENT OFFICE



Applicants: Tarek Eldin et al.

Assignee: Xilinx, Inc.

Title: Method of Automatic Fault Isolation in a Programmable Logic Device

Serial No.: 10/815,492 Filing Date: April 1, 2004

Examiner: Saqib J. Siddiqui Art Unit: 2138

Docket No.: X-1481 US Conf. No.: 8518

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

OKay  
To  
Enter  
11-30-06  
S.S.

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the final Office action mailed from the Patent Office on September 27, 2006, the undersigned requests that above-referenced patent application be amended as follows, in accordance with 37 C.F.R. § 1.116(b)(1), (2), or (3).

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 8 of this paper.